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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/720,934	11/24/2003		Reza M. Golzarian	ITL.1055US (P17792)	5240
21906	7590	05/04/2006		EXAMINER	
TROP PRUI		•		BREWSTER,	WILLIAM M
8554 KATY I SUITE 100	FREEWA	ΛY		ART UNIT	PAPER NUMBER
HOUSTON,	TX 770	24		2823	

DATE MAILED: 05/04/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	
	10/720,934	GOLZARIAN, REZA M.	
Office Action Summary	Examiner	Art Unit	
	William M. Brewster	2823	
The MAILING DATE of this communicat	ion appears on the cover sheet wi	th the correspondence address -	••
Period for Reply	DEDLY IO OFT TO EVOIDE - ***	ONITH (O) OD THUDTY (OO) OA	/ C
A SHORTENED STATUTORY PERIOD FOR WHICHEVER IS LONGER, FROM THE MAIL - Extensions of time may be available under the provisions of 37 after SIX (6) MONTHS from the mailing date of this communic. - If NO period for reply is specified above, the maximum statutor. - Failure to reply within the set or extended period for reply will, Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	ING DATE OF THIS COMMUNIC CFR 1.136(a). In no event, however, may a relation. The period will apply and will expire SIX (6) MON to statute, cause the application to become AB	CATION. Peply be timely filed THS from the mailing date of this communication ANDONED (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed o	n <u>23 <i>March 2006</i>.</u>		
,	☐ This action is non-final.		
3) Since this application is in condition for	·		s is
closed in accordance with the practice t	ınder <i>Ex par</i> te <i>Quayl</i> e, 1935 C.D	. 11, 453 O.G. 213.	
Disposition of Claims			
4)⊠ Claim(s) <u>1-25</u> is/are pending in the appl	ication.		·
4a) Of the above claim(s) 2,3,8,12,13,15		n consideration.	
5)⊠ Claim(s) <u>1,4,5,9,10 and 21-25</u> is/are all	owed.		
6)⊠ Claim(s) <u>11,14,16 and 18</u> is/are rejected	d.		
7) Claim(s) is/are objected to.			
8) Claim(s) are subject to restriction	n and/or election requirement.		
Application Papers			
9) The specification is objected to by the E	xaminer.		
10) The drawing(s) filed on is/are: a)		by the Examiner.	
Applicant may not request that any objection	n to the drawing(s) be held in abeyar	ice. See 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the			
11)☐ The oath or declaration is objected to by	the Examiner. Note the attached	Office Action or form PTO-152	2.
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for	foreign priority under 35 U.S.C. §	119(a)-(d) or (f).	
a) ☐ All b) ☐ Some * c) ☐ None of:	J , , ,	. , , , , ,	
1. Certified copies of the priority doc	cuments have been received.		
2. Certified copies of the priority doc		pplication No	
3. Copies of the certified copies of t	he priority documents have been	received in this National Stage	:
application from the International	Bureau (PCT Rule 17.2(a)).		
* See the attached detailed Office action for	or a list of the certified copies not	received.	
Attachment(s)			
1) Notice of References Cited (PTO-892)		Summary (PTO-413)	
2) Notice of Draftsperson's Patent Drawing Review (PTO-		s)/Mail Date nformal Patent Application (PTO-152)	
 Information Disclosure Statement(s) (PTO-1449 or PTO Paper No(s)/Mail Date 	5) Notice of t 6) Other:		

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DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 11, 14, 16, 18 are rejected under 35 U.S.C. 102(a) as being anticipated by Hynix Semiconductor Inc., KR 2003002424 A.

Hynix anticipates

limitations from claim 11, a method comprising: in the figure,

implanting a semiconductor wafer 10; and

activating the implanted species by exposing said wafer to laser energy from an infrared laser 20, Detailed Description;

limitations from claim 14, the method of claim 11 including applying heat to said wafer, wherein infrared is or is transferred to heat;

limitations from claim 16, the method of claim 14 including applying heat using a laser 20, Detailed Description;

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limitations from claim 18, the method of claim 16 including mechanically perturbing said wafer at the same time said wafer is being heated, inherent (see explanation below).

For claim 18, Hynix does not enumerate the underlying thermodynamics and physics of the energy transfer of a laser. Proffered as evidence Martinis et al., US Patent No. 5,634,718 does highlight the physics. In col. 5, lines 34-52, Martinis explains that heat causes vibrations of the atoms. Vibrating is a form of mechanical perturbation. Therefore, Yamamoto's laser, which heats the wafer, causes vibrations and hence mechanical perturbations.

Allowed Claims

Claims 1, 4, 5, 9, 10, 21-25 are allowed.

The following is a statement of reasons for the indication of allowable subject matter in claim 1: the limitation, "exposing an implanted wafer to an infrared laser: and exposing said implanted semiconductor wafer to a second laser at energy lower than the energy of said infrared laser," cannot be found in the prior art of record.

Response to Arguments

Applicant's arguments with respect to filed claims have been considered but are moot in view of the new ground(s) of rejection.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to William M. Brewster whose telephone number is 571-272-1854. The examiner can normally be reached on Full Time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

William M. Brewster WILLIAM M. BREWSTER

PRIMARY EXAMINER

27 April 2006 WB